

EMBEDDED

DVx Sets New Standard for Digital Video 1
 C-Cube’s DVx is the first single-chip solution that performs real-time MPEG-2 encoding, which could enable digital video editing in consumer products. In a two-chip configuration, DVx enables professional-quality editing.

Editorial: Asian Vendors Take Up Alpha Torch 3
 With Digital planning to stop selling Alpha chips, it will be up to Samsung and Mitsubishi to push Alpha into the PC market.

Most Significant Bits 4
 Intergraph accuses Intel of “oppression;” Intel competitors unify behind AMD 3D; ATI claims performance lead in mobile 3D; Intel works around Pentium bug; 21164PC appears in \$2,000 systems; National/Cyrix merger complete; Cyrix targets office, living room; Erratum: Power3 manufacturing cost.

Embedded News 9
 Windows CE 2.0 handhelds swap CPUs; NKK debuts integrated R4650 MIPS chip; VLSI shrinks GSM logic to single chip; Philips licenses R4300 from NEC; Vadem turns x86 board into iPump; IBM 403GCX runs windows CE; Clarification: NEC R41xx.

ARM9 Doubles ARM Performance in ‘98 10
 The latest ARM core extends the pipeline to allow clock speeds of up to 150 MHz in a 0.35-micron process, significantly improving performance over that of its predecessor with minimal added silicon.

StrongArm-1500 Grapples With MPEG-2 12
 The latest application-specific version of StrongArm, aimed at set-top boxes, pairs the ARM core with a DSP-like “media processor” capable of real-time MPEG-2 decoding and audio processing.

Hyperstone Drops Power Below 100 mW 15
 The latest version of the E1, a CPU with DSP extensions, is compatible with its predecessors but offers much lower power consumption.

Cyrix Creates Ultimate CPU for Games 16
 The MXi, due in 2H98, combines the Cayenne CPU core with an advanced 3D accelerator and other system logic to form a low-cost PC processor with excellent 3D performance.

Hal Packs Sparc64 Onto Single Chip 19
 Hal Computer’s Sparc64-III incorporates its original six-chip processor onto a single die while offering several functional improvements, including compatibility with Sun’s Solaris OS and UPA bus.

The Slater Perspective: AMD Seeking Leadership Role 23
 Rather than simply following Intel, AMD has seized the initiative with its 3D extensions and plans for a “Slot A” interface.

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