References to Suggested Readings

A

Application Push; x

B

Books and Articles on Computer Architecture and Microprocessors; xiii
Branch Prediction; 108

C

Cache Design; 109
Complete Description of the x86 Instruction Set Architecture; 64

D

Data Communications and Networking; 367
Design and Implementation of High Performance Pipelines; 161
Design for Testability; 10
Digital and Integrated Circuit Design; 190

E

Early Environment Substitution Techniques; 186
Environment Substitution; 138
Examples of Platform and Systems-Related Literature Resources; xv
Expanding Microinstructions; 144

F

Full-Custom Design; 21

I

Instruction Level Parallelism; 181
Instruction Set Design; 15
Instruction Window; 76
ISA Bus; 386

K

K6 3D Technology; 75

M

MESI Protocol; 110
Model Specific Registers; 95

O

Operand Forwarding; 236

P

PC Design Guides; 325
PCI Bus; 400
Pipeline Design; 302
Plug and Play; 339
Precise Interrupts; 178
Processor Local Buses; 425
Processor Memory Mismatch Problem; 112

R

Register Bypassing; 236
Register Renaming; 94
Reorder Buffer; 308
Reorder buffer; 134
Reservation Station; 76
Return Address Stacks in Microprogrammable

Processors; 216

S

Semi-Custom Design; 21

T

Technology Pull; x
The Evolution of Architecture Description
Languages; 20
The von Neumann Machine; 14

U

USB; 375