

Chapter 4

Figures

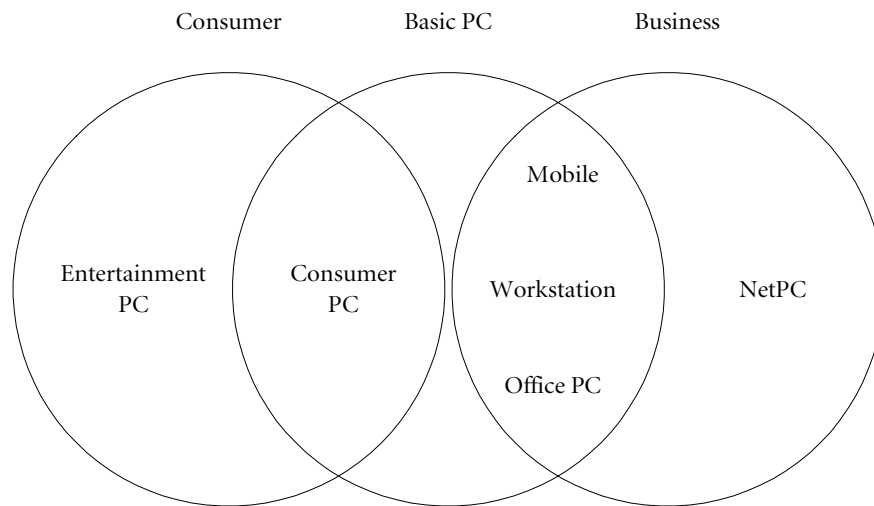


Figure 4.1 PC PLATFORM CATEGORIES AND SUBCATEGORIES

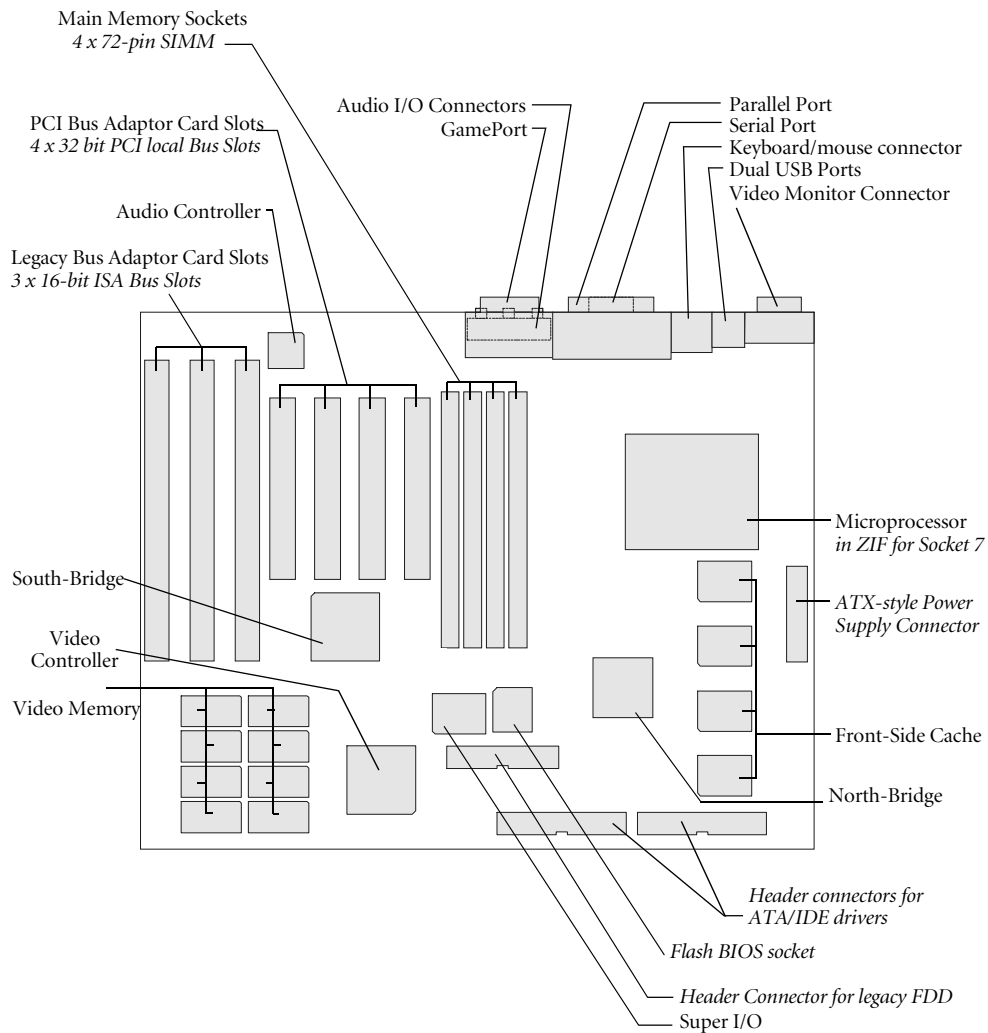


Figure 4.2 EXAMPLE ATX MOTHERBOARD LAYOUT

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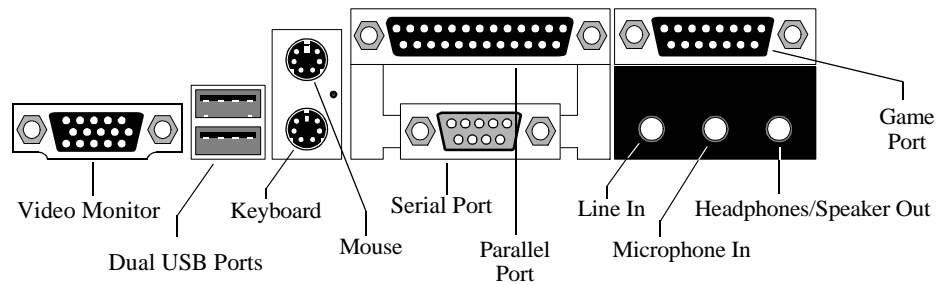
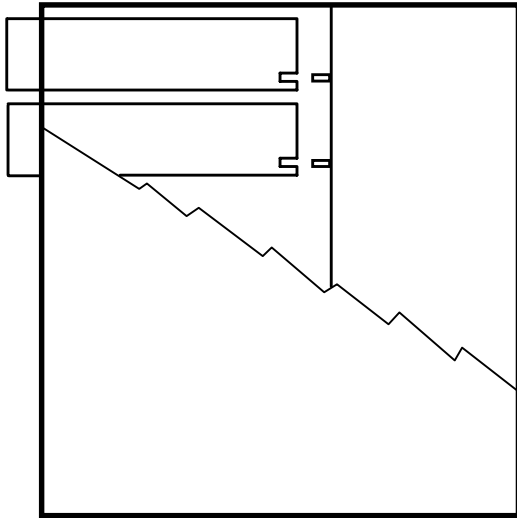
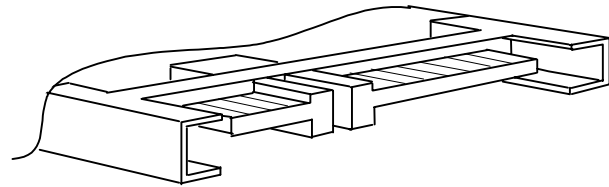


Figure 4.3 EXAMPLE ATX CONNECTOR PLACEMENT VIEWED FROM REAR OF BOARD

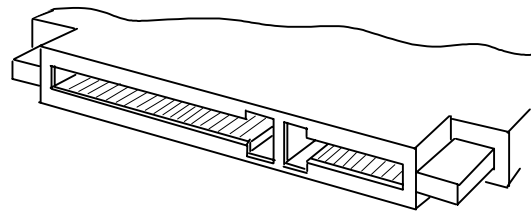
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**Abstract Cut-Away of System Unit
with Ejected Device Bay Devices**



**Recessed Connector in Rear of
Removable Device**



Connector in System Device Bay

Figure 4.4 DEVICE BAY CONCEPTUAL DRAWINGS

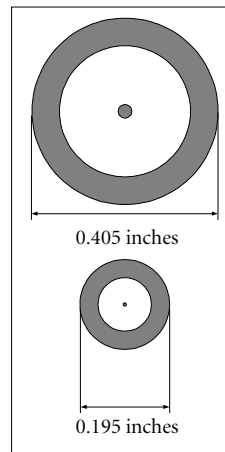


Figure 4.5
COMPARISON OF
THICK AND THIN
COAXIAL CABLES
USED FOR ETHERNET

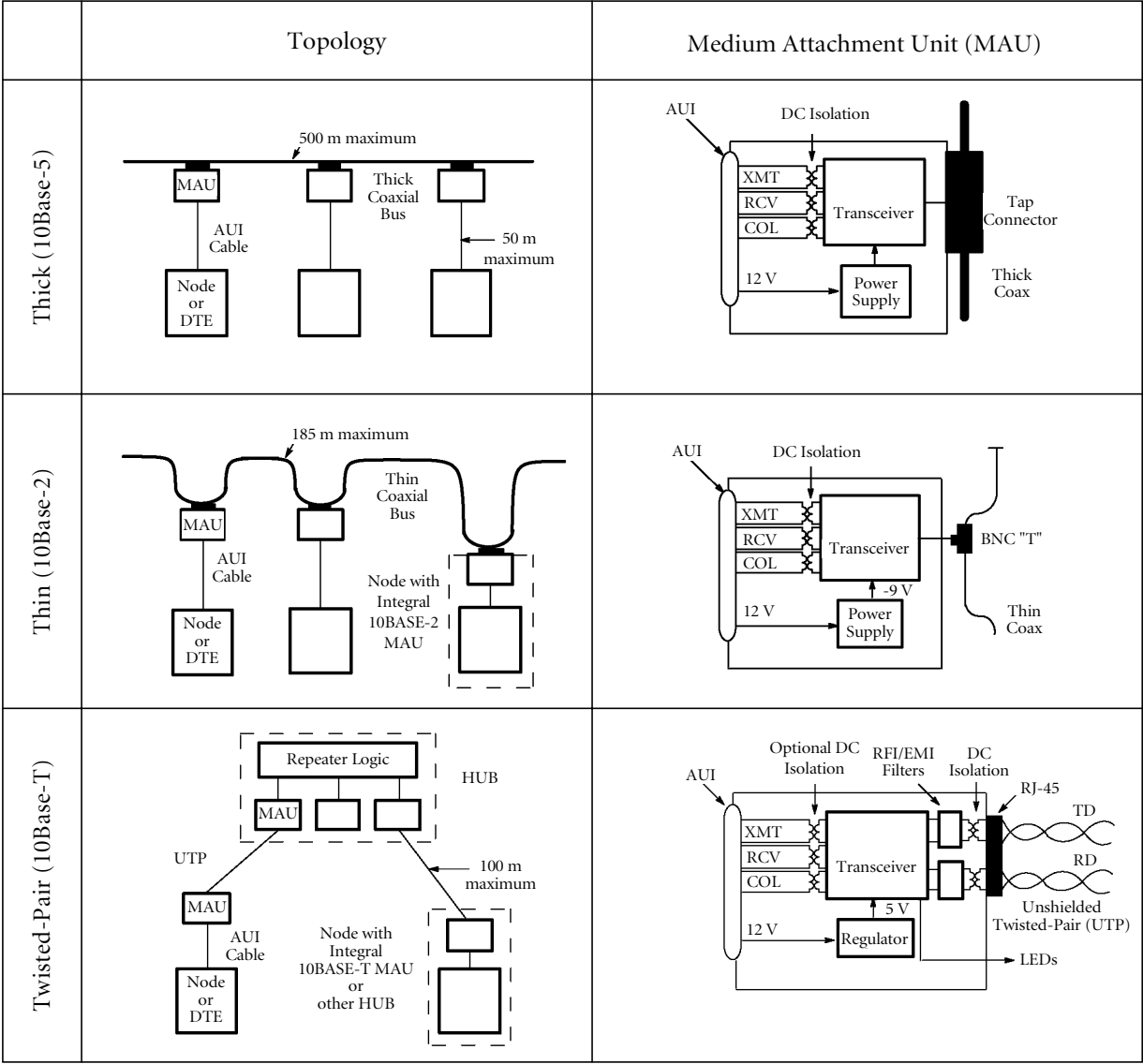


Figure 4.6 COMPARISON OF ETHERNET TYPES

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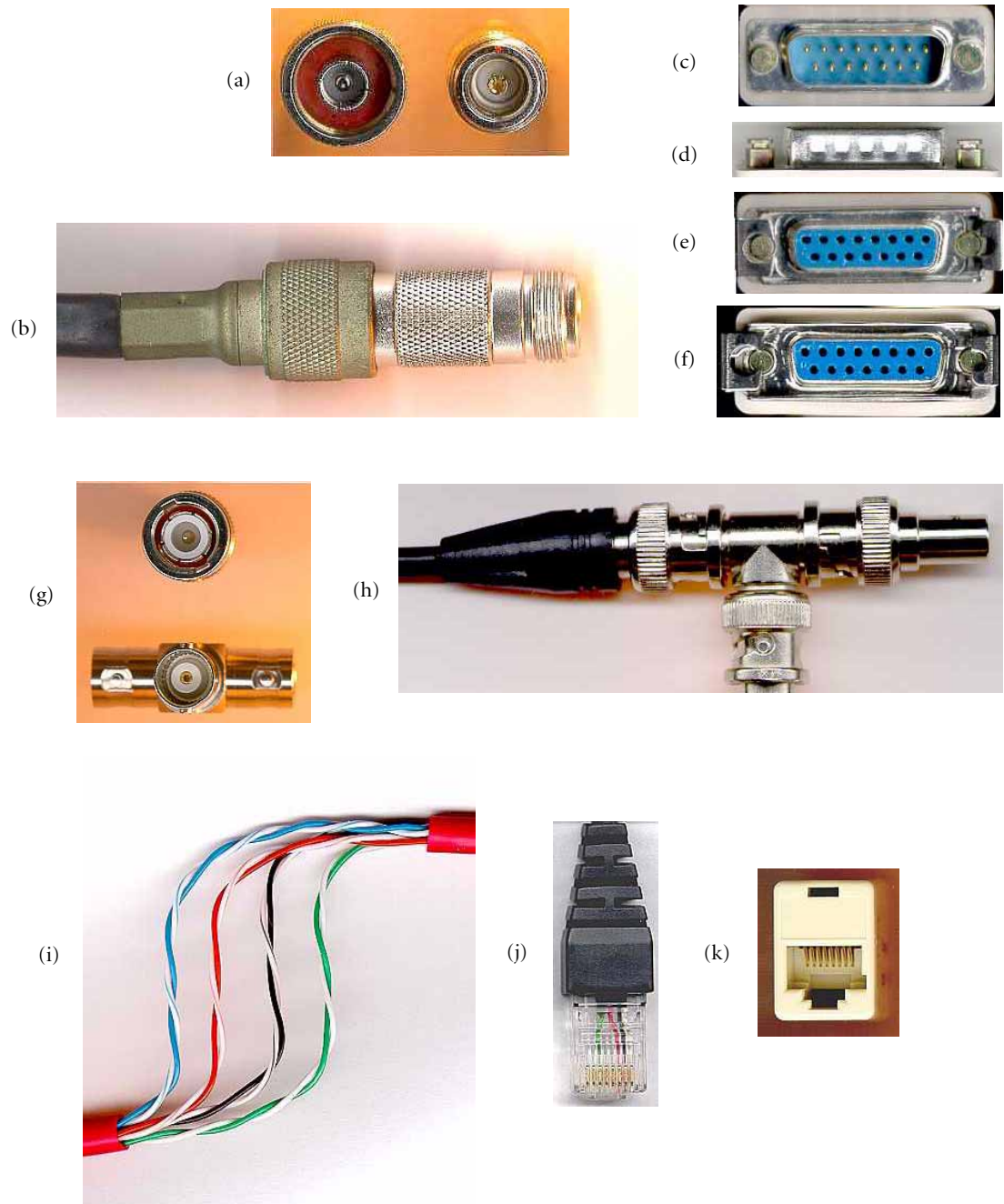


Figure 4.7 COMMON ETHERNET CONNECTORS AND CABLES

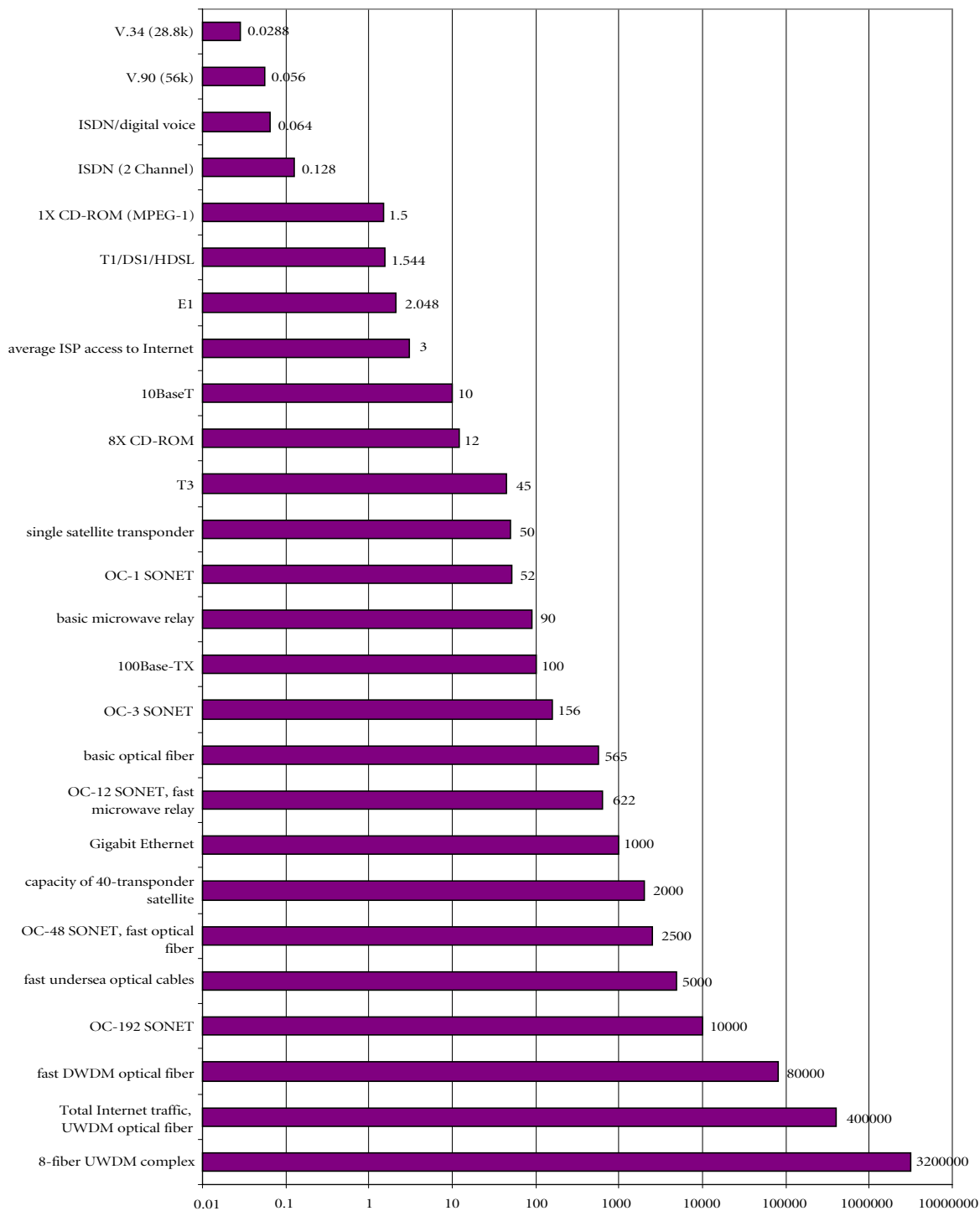


Figure 4.8 BANDWIDTH OF CHANNELS AND LINKS (BANDWIDTH IN MBITS PER SECOND, LOGARITHMIC SCALE)

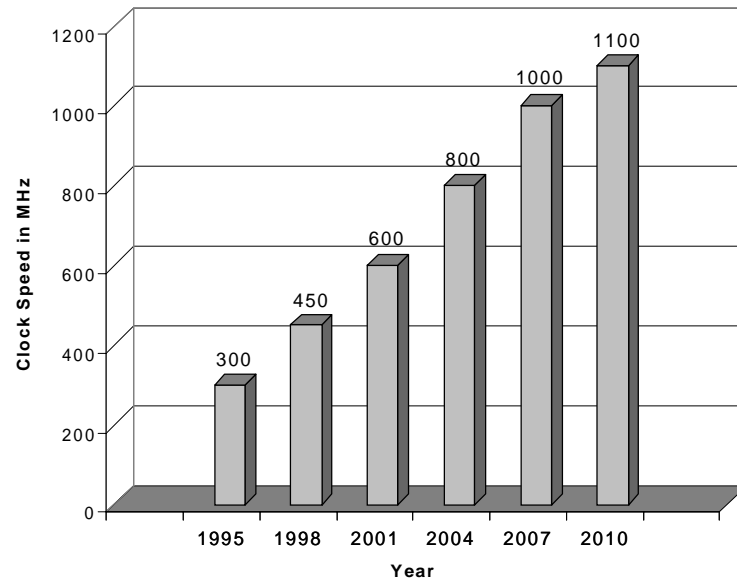


Figure 4.9 EXPECTED GROWTH IN MICROPROCESSOR CLOCK SPEED
Based on Semiconductor Association (SIA) Forecast

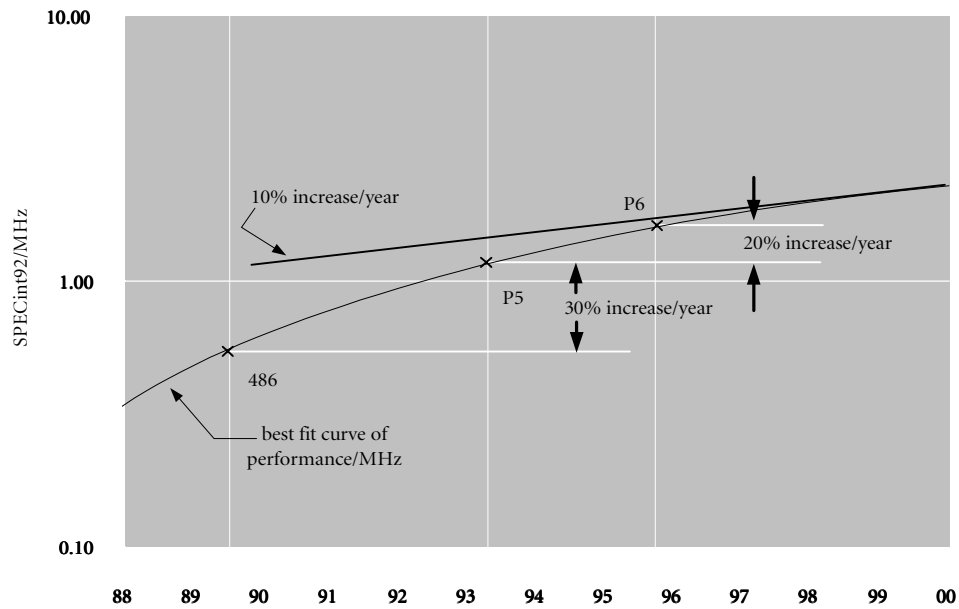


Figure 4.10 MICROARCHITECTURAL PERFORMANCE VS TIME

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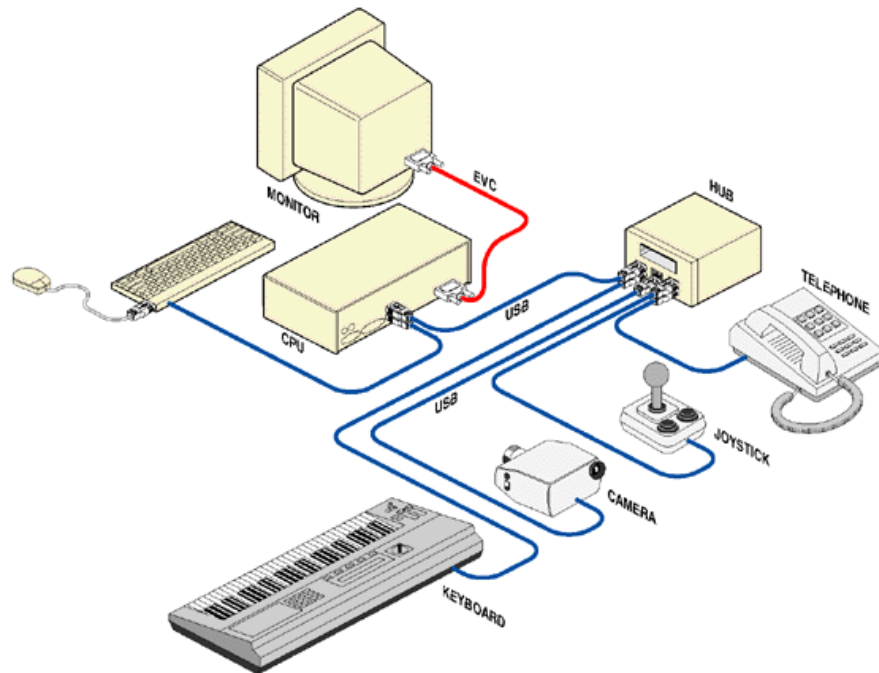


Figure 4.11 EXAMPLE USB INTERCONNECT USE IN A SMALL SYSTEM
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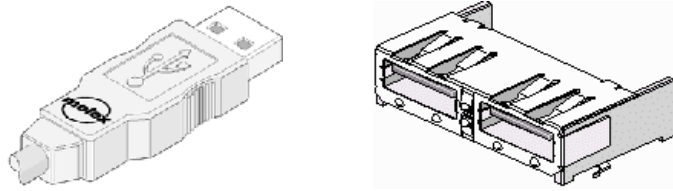


Figure 4.12 USB CONNECTOR AND PORT
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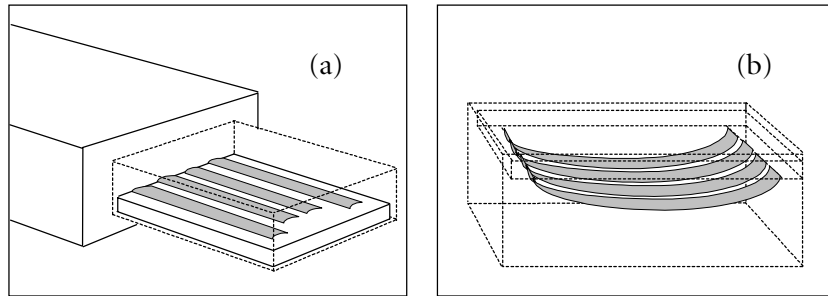


Figure 4.13 DRAWING OF USB CONNECTOR AND PORT INTERNAL DETAIL

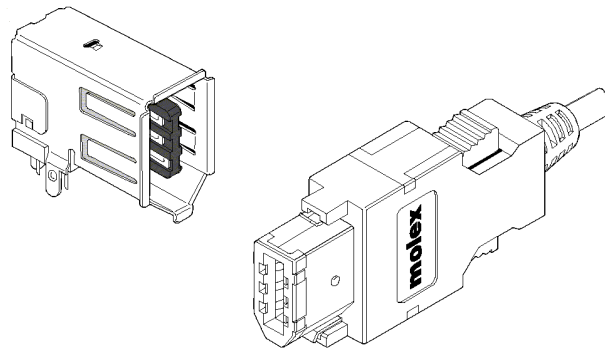
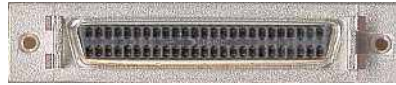


Figure 4.14 SIX-WIRE EXTERNAL PORT AND PLUG CONNECTOR
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50-pin Centronics-style male



50-pin high-density female



68-pin high-density male

Figure 4.15 COMMON SCSI CONNECTORS

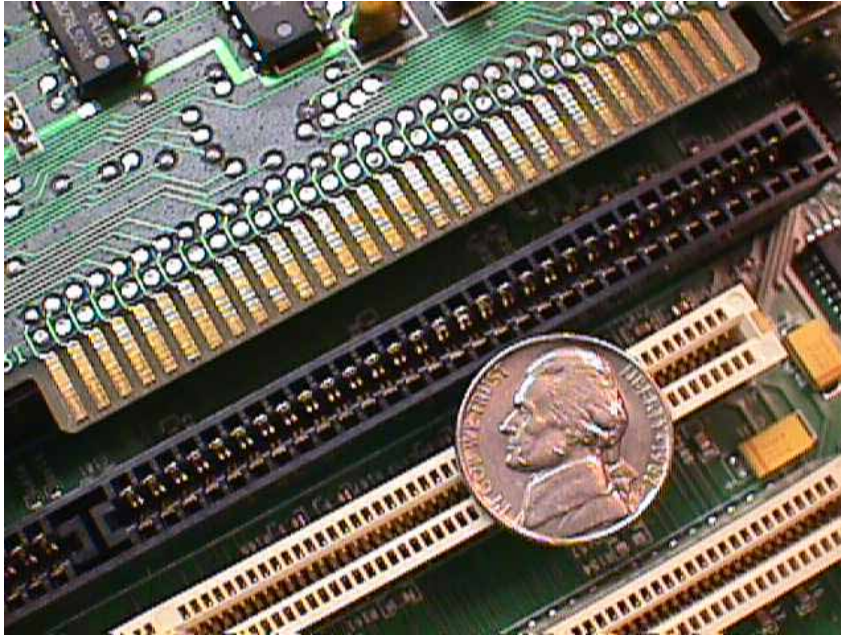


Figure 4.16 VIEW OF 8-BIT ISA CARD EDGE ADJACENT TO 8-BIT PORTION OF ISA CARD SLOT

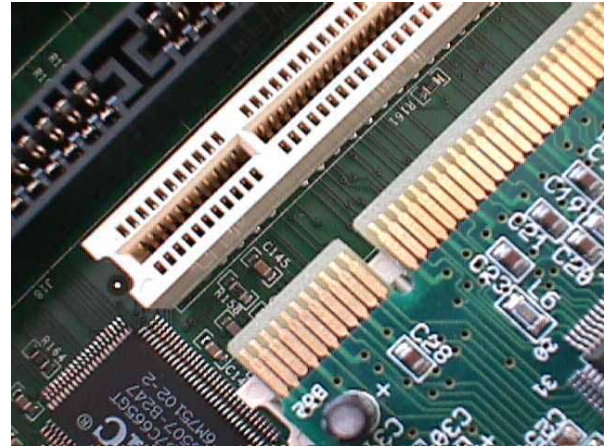
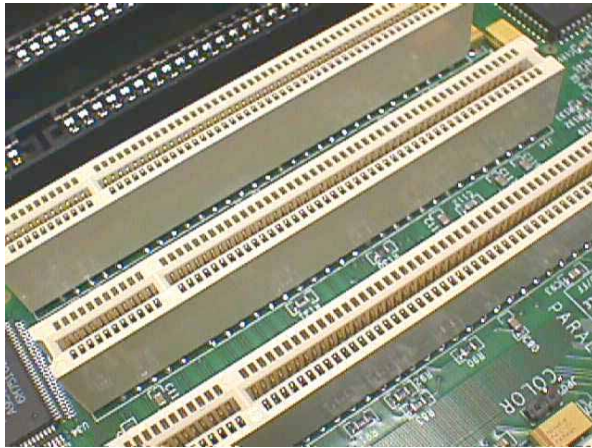


Figure 4.17 VIEWS OF PCI CARD SLOTS AND PCI CARD EDGE

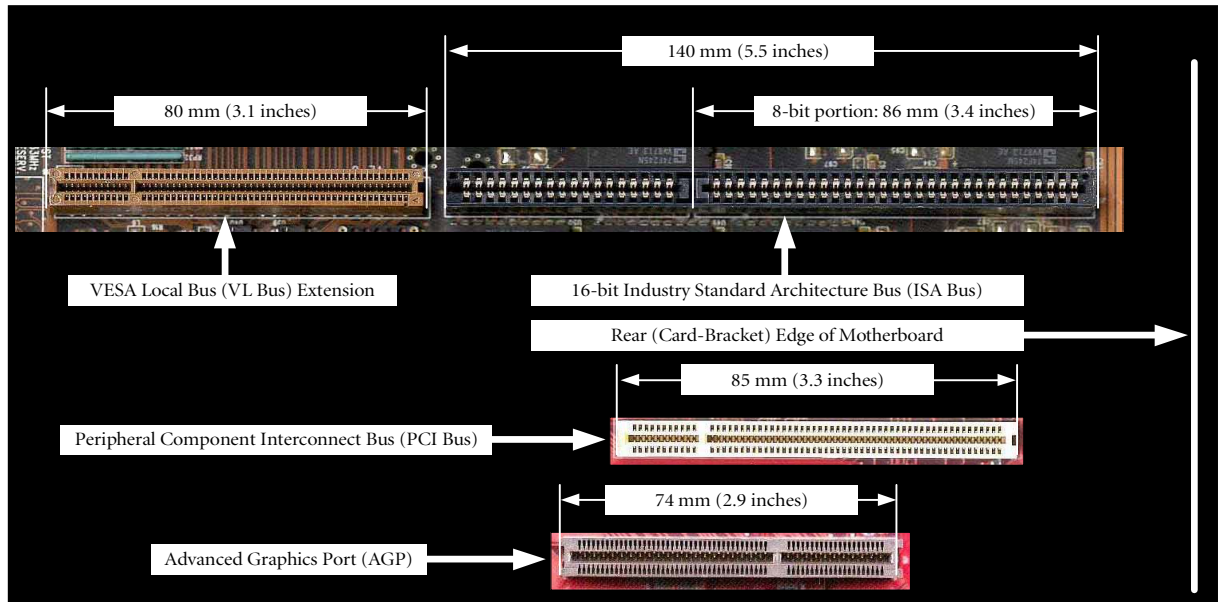


Figure 4.18 COMPARISON OF POPULAR CARD SLOTS

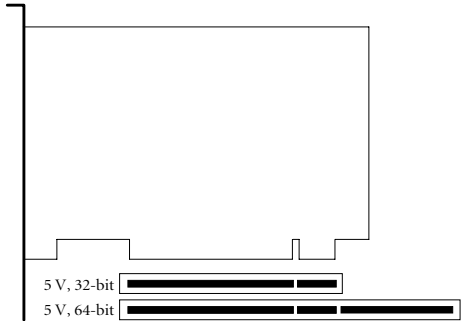
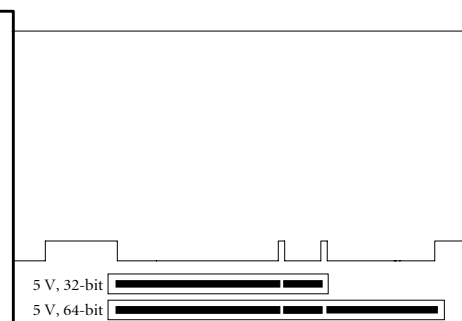
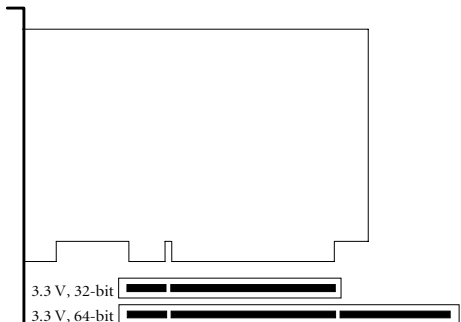
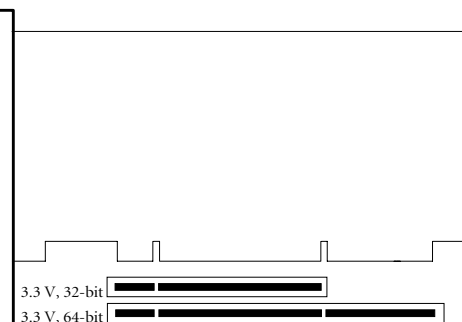

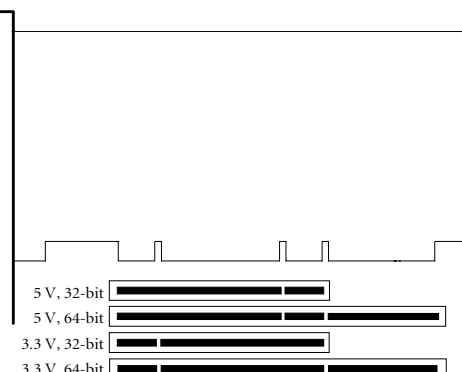
	32-bit	64-bit
5 V		
3.3 V		
"Universal"		

Figure 4.19 MATRIX OF PCI CARD TYPES AND ASSOCIATED COMPATIBLE SLOTS

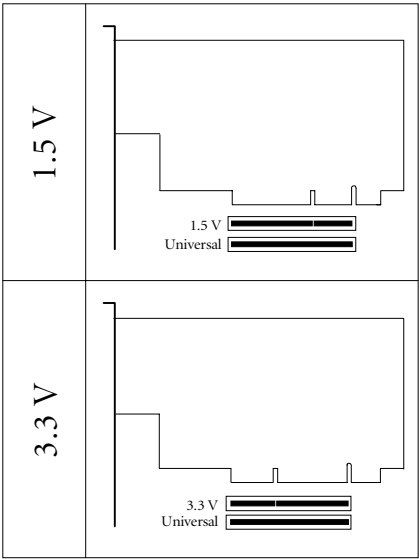


Figure 4.20 AGP CARD TYPES AND ASSOCIATED COMPATIBLE CONNECTORS

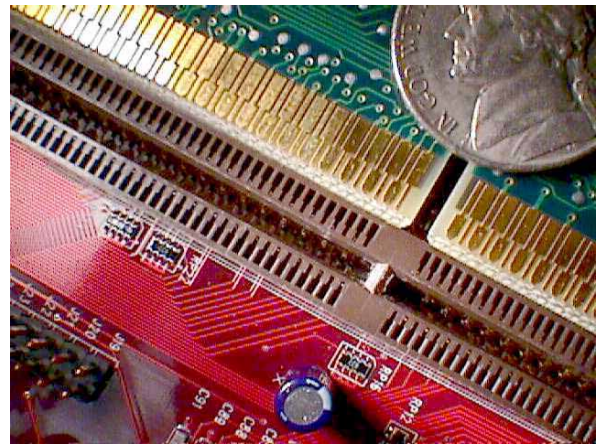
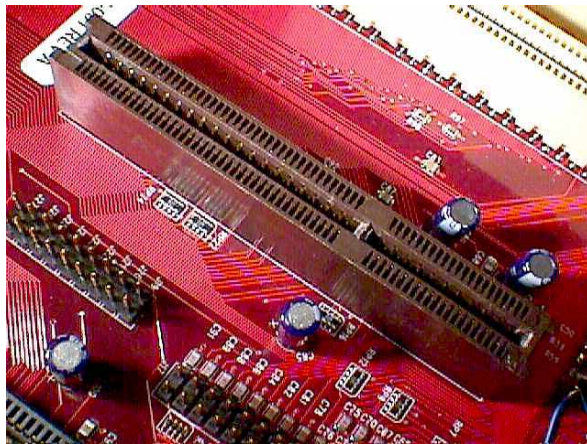


Figure 4.21 VIEWS OF AGP SLOT AND AGP CARD EDGE

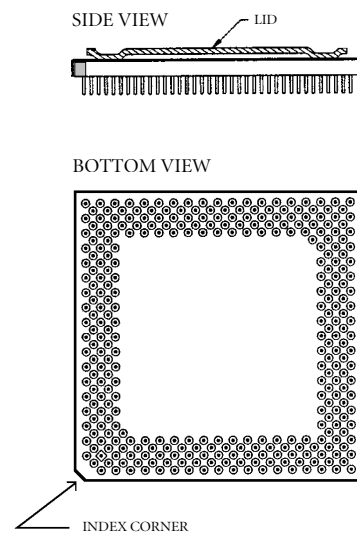


Figure 4.22 321-PIN
SPGA PACKAGE

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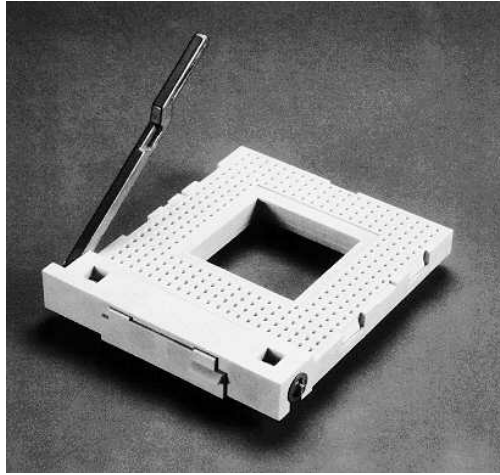


Figure 4.23 AMP ZIF
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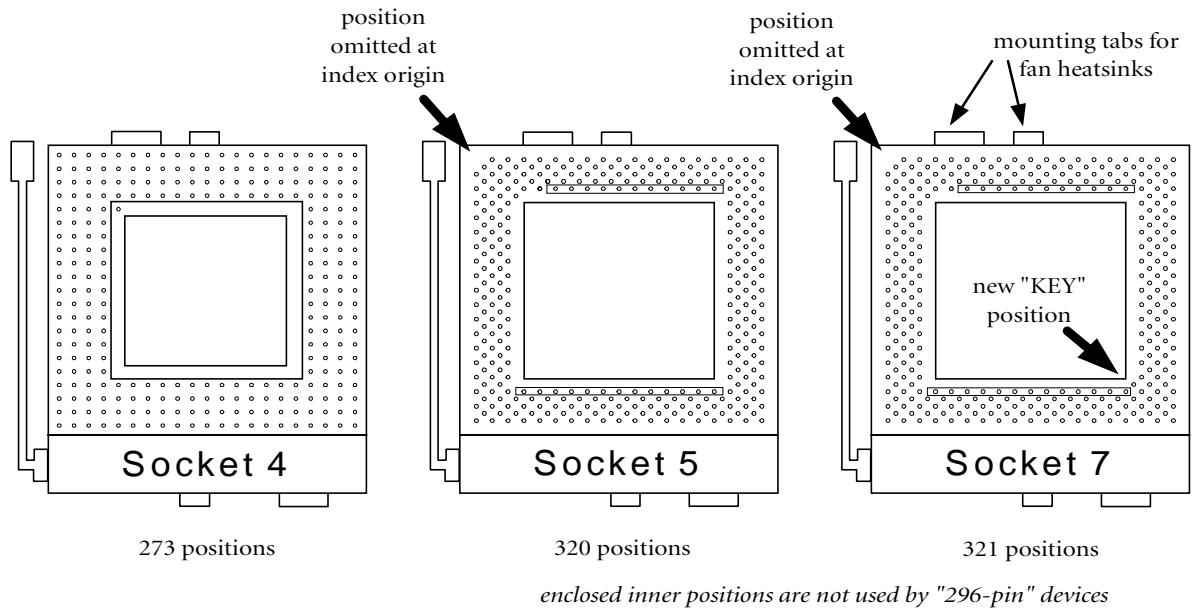


Figure 4.24 SAME SCALE COMPARISON OF SOCKETS 4, 5 AND 7

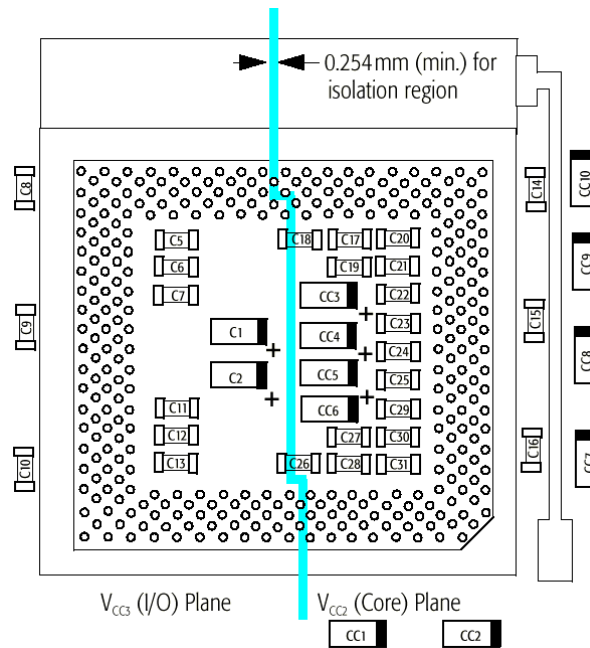


Figure 4.25 SPLIT POWER PLANES FOR DUAL-SUPPLY PROCESSORS
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