RESOURCES

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❖ 1394—ASK FOR IT BY NUMBER

We may never get a mainstream PC chip set with integrated 1394 support, but we don't have long to wait for the **1394 Developers' Conference 2000**, which will be held June 20–22 at the Fairmont Hotel in San Jose. Along with keynotes from Panasonic, Microsoft, and Sony executives, attendees will receive the latest updates in the progress of the 1394b and 1394.1 standards, copy-protection extensions, OS support, and even wireless 1394 implementations.

On-site registration is $900. For more information, visit the 1394 Trade Association's Web site at *www.1394ta.com* or give them a call at 425.489.9494.

❖ PLAN ON PLATFORM

If you want to know what the leaders in PC platform technology—other than Intel, that is—are planning to bring us over the next year or two, you'll want to sign up for InQuest Market Research's **Platform Conference**, July 18–19 at the Silicon Valley Conference Center in San Jose. The conference will feature vendor presentations on topics such as microprocessors, chip sets, DRAM, graphics, and I/O for systems both large and small.

Early conference registration (by June 30) costs $695. A quantity discount (buy two, get one free) is available. For more information, call InQuest at 480.813.7785 or visit the conference Web site at *www.platformconference.com*.

❖ MAKE MINE MAC

On the other hemisphere, right-brained readers may prefer to spend July 18–21 at the Javits Convention Center in New York, attending the **Macworld Conference and Expo**. Don't expect to learn much about Macintosh system architecture at this show, however; Apple does it all for you.

Registration fees range from $10 (for exhibits only) to $1,195 (for everything) if received by June 19. More information is available online at *www.macworldexpo.com*.

❖ INTEL RELEASES ITANIUM DOCUMENTATION

Intel has released the Itanium Processor Microarchitecture Reference, a guide to the Itanium implementation of the IA-64 architecture. The reference covers topics such as cache hierarchies, memory management, and instruction execution latencies. It may be downloaded from *developer.intel.com/design/ia-64/downloads/245473.htm*. ❖