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EMBEDDED TIDBITS

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CADENCE TO SPIN OFF ALCHEMY

Austin-based startup Alchemy Semiconductor (formerly Alchemy Microprocessor Design Group) has landed $15 million in first-round venture funding and is preparing to spin off from parent company Cadence Design Systems. The financing was provided by U.S. Venture Partners, Austin Ventures, and Telos Ventures. Alchemy was founded last year by a group of former Digital Semiconductor engineers who are veterans of the StrongARM design team (see *MPR 4/19/99-en*, "StrongARM Team Transmutes Into Alchemy"). The company obtained a MIPS32 license from MIPS Technologies and is working on a highly integrated embedded processor for networking applications. The processor is expected to deliver high performance (in the 500MHz range) and low power consumption (as low as 200mW). Alchemy plans to reveal its design at Embedded Processor Forum on June 13. For more information: *www.alchemysemi.com*. —*T.R.H.*

STRONGARM DESIGN WIN: IPAQ POCKET PC

Compaq is using Intel's StrongARM SA-1110 processor and StrataFlash memory in its iPAQ Pocket PC, which runs the latest version of Microsoft's Windows CE operating system for consumer-oriented embedded applications. The 206MHz SA-1110 has a first-generation StrongARM core and an integrated memory controller, and it typically consumes less than 500mW (see *MPR 4/19/99-03*, "Intel Flexes StrongARM With New Chips"). The iPAQ is one of several new handheld PCs that Microsoft hopes will rejuvenate Windows CE, which is now known as Windows for Pocket PCs. For more information: *www.intel.com/pressroom*. —*T.R.H.*

MOSYS 1T-SRAM GAINS MOMENTUM

Chartered Semiconductor has verified MoSys 1T-SRAM on its standard 0.25-micron logic process, opening the door for system-on-a-chip developers to integrate the unique memory technology into their designs (see *MPR 9/13/99-05*, "MoSys Explains 1T-SRAM Technology"). Chartered previously verified 1T-SRAM on its 0.18-micron logic process, so both options are now available. MoSys also concluded a deal with UMC to make 1T-SRAM available on that company's logic processes as well. For more information: *www.mosys.com*, *www.charteredsemi.com*, and *www.umc.com*. —*T.R.H.*

MICROWARE PORTS OS-9 TO MIPS

Microware has agreed to port its OS-9 embedded RTOS to the MIPS32 and MIPS64 architectures from MIPS Technologies. As part of the deal, Microware will also provide DAVID (digital audio/video interactive decoder), a software platform for digital-TV applications. DAVID uses Sun Microsystems Java and supports digital broadcast standards in the U.S., Europe, and other parts of the world. For more information: *www.mips.com* and *www.microware.com*. —*T.R.H.*