

EMBEDDED TIDBITS

{4/3/00-12}

◆ LEXRA RAISES \$7.5 MILLION

Lexra has raised \$7.5 million in second-round financing, which will come in handy as the company continues developing new cores for the embedded-processor market. The company is also fighting a patent-infringement lawsuit by MIPS Technologies over its MIPS-like cores (see [MPR 12/6/99-03](#), "MIPS vs. Lexra: Definitely Not Aligned"). Founded in 1997 and based in Waltham, Mass., Lexra offers processor cores that are largely compatible with the MIPS architecture. For more information: www.lexra.com. —T.R.H

◆ LSI LOGIC LICENSES MOsYS MEMORY

LSI Logic has licensed 1T-SRAM memory technology from MoSys for use in future ASICs and ASSPs (application-specific standard products). The addition of 1T-SRAM to LSI's CoreWare library will allow chip designers to integrate several megabits of SRAM with a processor core, using standard logic processes—unlike embedded DRAM, which requires a more specialized IC process (see [MPR 9/13/99-05](#), "MoSys Explains 1T-SRAM Technology"). For more information: www.lsillogic.com. —T.R.H

◆ MOTOROLA LICENSES M-CORE TO JAPANESE

Motorola has licensed its M-Core embedded-processor architecture to Yamaha and a Japanese university consortium, VDEC (VLSI Design and Education Center). Yamaha says it intends to use M-Core microcontrollers in mobile

phones, portable game consoles, mobile multimedia terminals, and home networking equipment. The academic license to VDEC allows as many as 170 undergraduate-engineering programs at Japanese universities to use M-Core in academic designs. —T.R.H

◆ PATRIOT SCIENTIFIC DESIGN WIN: CALIPER PIG

Computer & Audiotechnik (Dortmund, Germany) has chosen Patriot Scientific's PSC1000 processor (see [MPR 4/15/96-01](#), "New Embedded CPU Goes ShBoom") to use in the data recorder of a pipeline-inspection tool. C&A is designing the data recorder for Analytic Pipe GmbH, a German pipeline service company, which will integrate it into a tool known as the Caliper Pig. For more information: www.ptsc.com. —T.R.H

◆ CIRRUS LOGIC DESIGN WIN: MP3 PLAYERS

S3's Diamond Multimedia division is using Cirrus Logic's Maverick EP7209 chip in its third-generation Rio digital-audio players. The new Rio players, scheduled to ship this spring, will support the Secure Digital Music Initiative (SDMI) requirements for copyrighted audio in MP3 and Windows Media formats. The EP7209 is the first single-chip solution that supports both of those digital-audio standards (see [MPR 11/15/99-03](#), "Cirrus Logic Makes Music With ARM"). For more information: www.cirrus.com. —T.R.H ◆

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