RECENT IC ANNOUNCEMENTS

| PART NUM BER | VENDOR | DESCRIPTIO N | PRICE/Q UANTITY | AVAILABILITY |
| :---: | :---: | :---: | :---: | :---: |
| DSP |  |  |  |  |
| DSP1695 | $\begin{aligned} & \text { Lucent } \\ & 800.372 .2447 \end{aligned}$ | Internet DSP processes 16 simultaneous voice and data calls; also supports $56-\mathrm{Kb} / \mathrm{s}$ modems, voice- and fax-over-IP, and wireless data transmissions. | \$120/25,000 | $\begin{aligned} & \text { Sample-2Q99 } \\ & \text { Prod.-3Q } 99 \end{aligned}$ |
| M ICRO PRO CESSO R |  |  |  |  |
| ST72511R4 | STM icroelectronics 781.861.2650 | 8-bit microcontroller offers $16 \mathrm{~K}-32 \mathrm{~K}$ ROM / OTP/EPROM , 512K-1M RAM, CAN 2.0 B passive I/F, and 44 multifunctional bidirectional I/ 0 lines. | \$2.50/100K | Prod.-Now |
| ST72531R4 | STM icroelectronics 781.861.2650 | 8-bit microcontroller offers 16 K ROM / OTP/EPROM, 256 KB E2PROM, CAN 2.0B passive I/F, and two 16 -bit timers. | \$5.00/100K | Prod.-Now |
| INTERFACE |  |  |  |  |
| AMAZON | $\begin{aligned} & \text { AMCC } \\ & 650.569 .3806 \end{aligned}$ | OC-48 system solution provides full-duplex SO NET mapping of ATM cells or packet-based traffic; UTO PIA Level 3 interface for ATM traffic. | \$325/1,000 | Prod.-Now |
| Si3000 | Silicon Laboratories 512.464.9306 | 16 -bit codec integrates 30 resistors, 22 capacitors, a transistor, and a microphone pre-amp to implement voice channel. | \$2.35/10,000 | $\begin{aligned} & \text { Sample-2Q99 } \\ & \text { Prod.-2Q } 99 \end{aligned}$ |
| CS4281 | Cirrus Logic $512.912 .3587$ | PCI audio controller provides Sound Blaster legacy game support and is M icrosoft PC97, PC98, and PC99 compliant. | \$4.00/10,000 | $\begin{aligned} & \text { Sample-20 } 99 \\ & \text { Prod.-3Q } 99 \end{aligned}$ |
| CS4299 | Cirrus Logic $512.912 .3587$ | AC97 v2.1-compliant codec provides S/PDIF digital output, 20 -bit stereo digital-to-analog, 18 -bit stereo analog-to-digital; delivers a 120 -db range. | \$1.95/10,000 | Prod.-2Q99 |
| M EM ORY |  |  |  |  |
| M 28W160T/B | STM icroelectronics 781.861.2650 | 16 Mb flash includes $5 \mu \mathrm{~A}$ standby current, zero recovery from power down, 400 -ns program latency; available in TSO P and $\mu \mathrm{BGA}$ packages. | \$8.75/10,000 | Prod.-Now |
| PIC16F873/874 | $\begin{aligned} & \text { M icrochip } \\ & 602.786 .7668 \end{aligned}$ | 8-bit flash offers 128 bytes E2PROM, operating at $2.0-5.5 \mathrm{~V} ; 5$-M IPS performance at 20 MHz , two 8 -bit and one 16 -bit timers on a PLCC package. | \$7.39/1,000 | Sample-Now <br> Prod.-Late 2Q99 |
| UPD488448FB | NEC Electronics 800.366-9782 | $128-\mathrm{M} \mathrm{b}$ DRAM offers $1.6-\mathrm{G} / \mathrm{s}$ data transfer rate, $2.5 \mathrm{~V} \pm 0.13 \mathrm{~V}$ power supply, 256 Kword $\times 16$-bit $\times 32$-bank, 1 -Kb cache in a 62 -pin BGA package. | \$85.00/unit | $\begin{aligned} & \text { Sample-2Q99 } \\ & \text { Prod-3Q } 99 \end{aligned}$ |
| M ISCELLANEO US |  |  |  |  |
| M PCxxx | Microchip 602.786.7668 | Precision system supervisors include seven voltage trip points, monitoring of 3- to 5-V systems, reset to 1.0 V on TO-92 pin configurations. | \$0.38/1,000 | Sample-Now <br> Prod.-Now |
| STLC3055 | STM icroelectronics 781.861.2650 | Single-chip SLIC offers on-hook transmission, on-chip DC-DC converter, 5.5 - to $15.8-\mathrm{V}$ positive supply in a 10 mm -square 44 -lead TQ FP package. | \$6.00/100K | Prod.-Now |
| C55460 | Cirrus Logic $510.420 .6675$ | Power-meter-on-a-chip consists of two delta-sigma analog-to-digital converters, on-chip reference, high-speed calculations; bidirectional serial I/F. | $\begin{aligned} & \$ 4.00 / 1,000 \\ & \text { Prod.-3Q } 99 \end{aligned}$ | Sample-Now |
| SYSTEM LO GIC |  |  |  |  |
| ZPSD813F | $\begin{aligned} & \text { WSI } \\ & 510.498 .1723 \end{aligned}$ | 5-V 128-KB ISP flash offers a 3,000-gate CPLD, extra I/ 0 , <br> JTAG programming I/F, 3.7-mA power drain in a 52 -pin PLDCC package. | \$6.37/25,000 | Prod.-Now |

