RECENT IC ANNOUNCEMENTS

| PART NUMBER DSP | VENDOR | DESCRIPTIO N | PRICE/Q UANTITY | AVAILABILITY |
| :---: | :---: | :---: | :---: | :---: |
| ADSP-2141 | Analog Devices 800.262.5643 | Chip for VPN networks transfers IPSec-encrypted data at OC-3 rates of 155 M bits/s, with hash/ encryption throughput of 640 M bps . | \$65/10,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-20 } 99 \end{aligned}$ |
| M ICRO PRO CESSO R |  |  |  |  |
| PIC16HV540 | $\begin{aligned} & \text { M icrochip } \\ & 602.786 .7668 \end{aligned}$ | Eight-bit one-time-programmable microcontroller with an on-chip regulator, 33 single-word instructions, $20-\mathrm{M} \mathrm{Hz}$ single-cycle operation. | \$1.34/1,000 | Prod.-1099 |
| INTERFACE |  |  |  |  |
| AD7894 | Analog Devices $800.262 .5643$ | ADC comes in 8 -pin package with 14 -bit precision, $\pm 10-\mathrm{V}$ signal capability, single 5 -V supply, and 160-Ksample/s throughput rate. | \$7.95/1,000 | Prod.-Now |
| S3049 | $\begin{aligned} & \text { AMCC } \\ & 619.450 .9333 \end{aligned}$ | Laser driver for OC-48 DW DM and SONET/SDH applications has auto laser-power control, safety alarms, and internal positive power source. | \$85/100 | Prod.-Now |
| S3053 | $\begin{aligned} & \text { AMCC } \\ & 619.450 .9333 \end{aligned}$ | Quad multiplexer can switch $0 \mathrm{C}-3,-12,-24$, or -48 data signals in DWDM, SO NET/SDH, and other high-speed systems. | \$92/100 | Prod.-Now |
| S3054 | $\begin{aligned} & \text { AMCC } \\ & 619.450 .9333 \end{aligned}$ | Dual $2 \times 2$ crosspoint switch fans out or multiplexes high-speed data and clock signals for $\mathrm{OC}-3,-12,-24$, or -48 systems. | \$86/100 | Prod.-Now |
| THS7002 | Texas Instruments 800.477.8924 | A $70-\mathrm{M} \mathrm{Hz}$ amplifier for ADSL receiver applications with programmable gain, independent shutdown, $\pm 4.5-\mathrm{V}$ to $\pm 16-\mathrm{V}$ supply range. | \$5.79/1,000 | Prod.-1Q99 |
| M EM O RY |  |  |  |  |
| ZPSD813F1V | $\begin{aligned} & \text { WSI } \\ & 510.498 .1723 \end{aligned}$ | Flash MCU integrates 128 K of flash memory, 32 K of $\mathrm{E}^{2}$ PRO M, 2 K of SRAM ; operating at 2 M Hz , drawing 1.6 mA . | \$8.95/25,000 | Prod.-Now |
| M 40Z300/W | STM icroelectronics 781.861.2650 | Nonvolatile RAM controller converts low-power SRAM s into NVRAM s; with reset output for power-on reset; in 28-pin SO IC package. | \$4.25/100,000 | Prod.-Now |
| $\begin{aligned} & 70 \mathrm{~V} 3579 \\ & 70 \mathrm{~V} 3569 \end{aligned}$ | $\begin{aligned} & \text { IDT } \\ & 800.345 .7015 \end{aligned}$ | Synchronous dual-port SRAM chips are organized as $32 \mathrm{~K} \times 36$ ('79) or $16 \mathrm{~K} \times 36$ (' 69 ) with $3.3-\mathrm{V}$ supplies and $100-\mathrm{M} \mathrm{Hz}$ data rates. | \$39.95/10,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-3Q99 } \end{aligned}$ |
| $\begin{aligned} & \text { 70V3389 } \\ & \text { 70V3379 } \end{aligned}$ | $\begin{aligned} & \text { IDT } \\ & 800.345 .7015 \end{aligned}$ | Synchronous dual-port SRAM chips are organized as $64 \mathrm{~K} \times 18$ ('89) or $32 \mathrm{~K} \times 18$ ('79) with $3.3-\mathrm{V}$ supplies and $100-\mathrm{M} \mathrm{Hz}$ data rates. | \$39.95/10,000 | $\begin{aligned} & \text { Samples-Now } \\ & \text { Prod.-3099 } \end{aligned}$ |
| M ISCELLANEO US |  |  |  |  |
| $\begin{aligned} & \text { CS4340 } \\ & \text { CS4341 } \end{aligned}$ | $\begin{aligned} & \text { Cirrus Logic } \\ & 510.249 .4244 \end{aligned}$ | DAC has $100-\mathrm{dB}$ dynamic range, audio sample rates of 2 kHz to 100 kHz , power on/off transient control, and operates at 3 V to 5 V . | $\begin{aligned} & \$ 1.95 / 1,000 \\ & \$ 2.15 / 1,000 \end{aligned}$ | Prod.-Now |
| DiskOnChip | $\begin{aligned} & \text { M-Systems } \\ & 510.413 .5950 \end{aligned}$ | Single-chip disk replacement measures $11.7 \times 21.3 \times 1.2 \mathrm{~mm}$, writes at $400 \mathrm{Kbytes} / \mathrm{s}$, reads at 1.2 M bytes $/ \mathrm{s}$; with 8 M capacity. | \$20/10,000 | Prod.-Now |
| 74LCX16244T | STM icroelectronics <br> 781.861.2650 | Three-state noninverting 16 -bit buffer in a TSSO P-48 package features power-down protection, latch-up performance, and 2-kV EDS immunity. | \$1.00/10,000 | Prod.-Now |
| S2076 | $\begin{aligned} & \text { AMCC } \\ & 800.755 .2622 \end{aligned}$ | Quad fibre-channel transceiver bundles four channels in a $23 \times 23-\mathrm{mm}$ 208-pin TBGA package. | \$65/100 | Prod.-Now |
| $\begin{aligned} & \text { M L6460 } \\ & \text { M L6461 } \end{aligned}$ | M icro Linear 408.433.5200 | Video-encoder chips integrate output filters, amplifiers, A/D converters, and M acrovision copy protection ('60) for DVD applications. | \$8/1,000 | Prod.-Now |

