

RESOURCES

Protected-Mode Programming by the Book

Tom Shanley and Mindshare have teamed to bring out *Protected Mode Software Architecture*, a new book on programming x86 processors in protected mode. The 300-page book covers entering, leaving, and managing protected-mode software, including task management, local and global descriptor tables, virtual paging, and virtual 8086 mode. Programming models cover the 386, 486, and Pentium processors outside of their familiar 8088 emulation mode.

The book is available from bookstores for \$29.95 or by contacting Addison-Wesley Developers Press (Reading, Mass.) at 617.944.3700 or via the Web at www.aw.com/devpress.

New Catalog of PCI Hardware Available

The latest edition of the *PCI Databook* has been published by Annabooks. The catalog covers all manner of PCI hardware including chip sets, controllers, plug-in boards, repeaters, single-board computers, BIOS chips, and development tools. Products are indexed by vendor, product, and function, and the catalog is updated twice a year.

The \$125 price of the catalog includes one semiannual update and a copy of the *PCI Handbook*. Discounts are available for multiple copies. To order, or for more information, contact Annabooks (San Diego, Calif.) at 800.462.1042 or 619.673.0870; fax 619.673.1432.

Web Resource for Design Engineers

Cera and Eg3 have jointly produced an *Internet Shareware Tool* for locating engineering-related information on the Internet. The software locates design sites, research information, and software, among others. Use of the tool is free, although the companies expect to begin charging for additional services in 3Q97. Check it out at www.eg3.com or www.cera2.com.

Trampling a Path to Known Good Die

Engineers and oenophiles can make their way to the third annual *Known Good Die Industry Workshop*, to be held in California's scenic Napa Valley. The workshop covers three days, commencing September 18, and is sponsored by Sematech and Microelectronics and Computer Technology Corporation. This year's conference is divided into six sessions and will include representatives of the European Commission's Good Die program, Japan's EIAJ, semiconductor vendors, and researchers.

Registration is free. For more information, or to register, contact MCC (Austin, Texas) at 512.338.3312.

Numerical Listing of Chips on the Web

Dutch technology aficionado Jaap van Ganswijk has created a numerically sorted list of IC part numbers, including microprocessors, useful for identifying or verifying part types. The *Chip Directory* is located at www.xs4all.nl/~ganswijk/chipdir/.

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CALENDAR REMINDER!

Attend the 1996 Microprocessor Forum
October 21-24, San Jose, Calif.

Mark your calendars now to attend the ninth annual Microprocessor Forum this October. Celebrating the twenty-fifth anniversary of the microprocessor, the Forum will feature special industry retrospectives as well as predictions for the next 25 years.

As always, the Microprocessor Forum will include announcements from industry leaders of the latest microprocessors for PCs and workstations, embedded applications, and multimedia. The Forum again offers evening affinity sessions for detailed discussions of cutting-edge issues and plenty of time to connect with colleagues.

To stay on top of the program for Microprocessor Forum, visit our Web site (www.chipanalyst.com). We will be updating the program information on a weekly basis beginning in June. See you in October!