AT A GLANCE

| Motorola's new 68HC12 instruction set extends the popular 8-bit HC11 design. The new chips maintain compatibility at the assembly code level but are not binary compatible. With the similar bus, peripherals, and programming model, HC11 programmers will feel at home with the new architecture. The first two HC12 devices offer lower performance than Motorola's 68HC16 products, but in price/performance they are competitive with other embedded chips. | . 1 |
|--|-----|
| Editorial: Bringing High Availability to the Masses Microsoft's forthcoming WolfPack software allows highly available systems to be built from off-the-shelf components. This technology will move x86 processors into the fault-tolerant market. | . 3 |
| Most Significant Labs | . 4 |
| Mitsubishi Mixes Microprocessor, Memory | 10 |
| PowerPC 603e, 604e Hit 200 MHz | 13 |
| The number-two system-logic vendor, Silicon Integrated Systems, unveiled three new chip sets that set new standards in integration. Genesis provides system logic and graphics for low-cost Pentium systems, while Trinity and Archer are single-chip system-logic solutions for high-performance Pentium and P6 systems, respectively. | 14 |
| Java Performance Advancing Rapidly Benchmarks on initial versions of just-in-time (JIT) compilers for Java show their performance to be about half that of optimized C code. Since this performance is likely to improve as JIT technology matures, this gap will leave little room for specialized Java chips. | 17 |
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Publisher and Editorial Director

Michael Slater E-mail: mslater@mdr.zd.com

Editor in Chief

Linley Gwennap E-mail: linley@mdr.zd.com

Senior Editor

Jim Turley E-mail: jturley@mdr.zd.com

Senior Analyst

Yong Yao E-mail: yyao@mdr.zd.com

Senior Analyst

Peter N. Glaskowsky E-mail: png@mdr.zd.com

Editorial Assistant: Suzanne Gifford

Editorial Board

Dennis Allison
Brian Case
Dave Epstein
John Novitsky
Nick Tredennick

Rich Belgard
Jeff Deutsch
Don Gaubatz
Bernard Peuto
John F. Wakerly

Editorial Office

480 San Antonio Rd., Suite 210 Mountain View, CA 94040 **Phone:** 415.917.3050 **Fax:** 415.917.3093

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President: Peter Christy E-mail: pchristy@mdr.zd.com

Business Office

874 Gravenstein Hwy. So., Suite 14 Sebastopol, CA 95472 **Phone:** 707.824.4004 **Fax:** 707.823.0504

Subscriptions: 707.824.4001 E-mail: cs@mdr.zd.com

World Wide Web: www.chipanalyst.com

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