At a Glance

Thumb Squeezes ARM Code Size1 Advanced RISC Machines has invented a new 16-bit instruction set, called Thumb, that can be translated with minimal hardware into 32-bit ARM code. Thumb improves code density and reduces system cost, particularly if 16- or 8-bit devices are used.
Editorial: Embedded Products Drive MIPS Success3 MIPS processors are primed for success in various embedded video applications, but its desktop processors are not faring as well.
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NEC Plunges into PDA Processor Market
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MICROPROCESSOR ♦ REPORT

THE INSIDERS' GUIDE TO MICROPROCESSOR HARDWARE

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