

# At a Glance

<b>IBM, Motorola Preview Embedded PowerPCs .....</b>	<b>1</b>
Motorola and IBM have both announced their first PowerPC chips targeted at embedded applications. The IBM 404 contains many integrated peripherals and appears to be targeted at midrange designs, while Motorola's 505 includes an FPU and is suited for high-end applications. Both offer excellent performance.	
<b>Editorial: Intel's Predicament .....</b>	<b>3</b>
Intel now provides system innovations for the entire PC industry, sometimes against the interests of Intel's largest customers.	
<b>Most Significant Bits .....</b>	<b>4</b>
HyperSparc hits 100 MHz; Sun expands Fujitsu relationship; Intel cuts back on DX4, pushes Pentium-60; IMS demonstrates x86 emulation chip; Compaq Aero uses 486SXJ; C-Cube chip decodes MPEG-2 and DigiCipher; Errata: Irda fax number.	
<b>IBM and Cyrix Ink Five-Year Pact .....</b>	<b>10</b>
IBM will manufacture x86 chips for Cyrix and will also sell these chips under the IBM label, creating a significant threat to Intel.	
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Simplifying the task of building multiprocessor systems, Intel has defined standard extensions to the PC architecture for MP. The new specification will increase the number of vendors of MP systems and should reduce the price of these systems.	
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The 68322 combines a 68EC000 core with an autonomous banding coprocessor to speed image processing for low-cost printers.	
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As the difficulty of building massively superscalar processors increases and the incremental benefits decline, one alternative is to place multiple independent processors on a single chip. This approach is likely to appear in mainstream products later this decade.	
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## MICROPROCESSOR REPORT

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