Resources

Networking in Silicon Valley

Whether you have to fly though a hub, drive a route, or cross a bridge, get to the **Silicon Valley Networking Conference** (SVNC) on April 5–7. The conference, held at the San Jose Hyatt, opens on Tuesday with a choice of two tutorials: ATM or high-speed interconnects. The main sessions, on Wednesday and Thursday, consist of two tracks, each with a variety of vendor presentations. The first track covers ATM and other high-speed networks, and the second discusses internetworking and network management.

Early registration (by 3/18) for any one day runs $195, or $395 for all three days. To register, contact SysTech Research (San Jose, Calif.) at 408.293.8383; fax 408.293.0901.

But Will It Also Find Old Socks?

To help users reuse old software, **Source Translation and Optimization (STO)** maintains a large database of previously published and publicly available software resources. This information can also be useful for software patents and in searching for prior art. The database contains information on more than 15,000 programs, 5,000 software patents, and 90,000 abstracts for algorithms and technology reports. STO tracks more than 250 journals and 150 research facilities as well as the Internet and other electronic distribution systems.

The company performs comprehensive searches and other consulting services. For more information, contact Gregory Aharonian of STO (Belmont, Mass.) at 617.489.3727 or e-mail *srctran@world.std.com*.

Running Out of Cache

Academic Press has published a new book that introduces system designers to the intricacies of cache design. ***The Cache Memory Book***, by Jim Handy, provides a tutorial on basic cache concepts, leading to a detailed description of the merits of different design techniques, and it includes a full chapter of example designs. The 250-page hardcover book costs $44.95 (ISBN 0-12-322985-5). To order, call Academic Press (Orlando, Fla.) at 800.321.5068; fax 800.874.6418.

Embedded Conference in Boston

For East Coast afficionados of embedded systems, the second annual **Embedded Systems Conference East** will be held in Boston's Hynes Convention Center on April 19–21. The Tuesday session will offer the choice of five full-day tutorials on topics such as fuzzy logic, digital signal processing, and embedded design methods. On Wednesday and Thursday, the conference will feature a variety of shorter workshops and lectures. An exhibit will show products from more than 65 vendors.

Early registration (by 4/8) for the conference costs $695, with an additional $395 for the tutorials. For more information, contact Miller Freeman, Inc. (San Francisco) at 415.905.2354; fax 415.905.2220.

New Report on High-End Embedded Market

For even more information on embedded systems, check out the new report from Desktop Strategies, ***Opportunities in the High-Performance Embedded Control Market***. The study describes the various 32-bit and 64-bit microcontrollers on the market today, focusing on their architecture, implementation, and product strategies. The report also analyzes markets such as consumer products, office automation, communications, and internetworking, listing historical and projected shipments, the leading vendors, and key application needs.

The report is priced at $1,850. To order, contact Desktop Strategies (Los Altos, Calif.) at 415.948.3927; fax 415.948.3590.