

At A Glance

Graphic Introductions Accelerate	1
Five companies—Cirrus, Weitek, Chip & Technologies, Oak, and Tseng Labs—announced high-performance graphics chips that use a variety of innovative techniques to break the memory-bandwidth bottleneck. Sierra also debuted a low-cost accelerator chip.	
Editorial: PDAs—The Next Big Processor Battle	3
The ever-increasing fervor over personal digital assistants (PDAs) has driven nearly every major processor vendor to at least start a PDA program, but it is too soon to predict the winner.	
Most Significant Bits	4
SuperSPARC leaps to 60 MHz—TI cuts price; AMD cleans 486 chips, adds 486SX; VLSI previews x86 PDA processors; AMD pursues palmtops with Elan; RISC vendors vie over future cable boxes; Data General rescues 88110 from oblivion; IBM RSC hits 45 MHz; LSI upgrades 33020 X-terminal controller; R4000 chip set slips into 1994; OmniBook, Zoomer processor wins revealed; Cyrix IPO reveals fab issues.	
Weitek Announces SPARC Upgrade Chip	12
Weitek's new SPARC Power chip can be plugged into any SPARC-station 2, offering a 40-50% performance boost. It uses a clock doubler to reach 80 MHz, and includes 24K of on-chip cache.	
VLSI Integrates 486SL Power Management	16
Scamp IV is a highly-integrated system-logic chip set for notebooks using the new "SL-Enhanced" 486 chips. Its power management is software-compatible with the original 486SL processor.	
Digital Reveals PCI Chip Sets For Alpha	20
DEC is designing two system-logic chip sets for the 21064 CPU. Both provide a bridge to PCI, but one focuses on workstation-level performance while the other is lower in cost.	
Energy Star PCs Debut	23
As the EPA announced its Energy Star program, the first wave of "green" PCs rolled out, but these initial systems offer little true power savings over previous designs.	
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