

Resources

New Book from Patterson and Hennessy

Professors David Patterson and John Hennessy have completed a follow on to their highly successful *Computer Architecture: A Quantitative Approach*. The new text presents the basics of computer organization and design while providing a complete introduction to assembly language via the MIPS architecture. Titled *Computer Organization and Design: the Hardware/Software Interface*, it is primarily aimed at those who are not computer architects but have other reasons for understanding computer design, such as compiler writers and other software engineers.

The text book costs \$64.95 and will be first shipped this summer. Contact Morgan Kaufmann, 2929 Campus Drive, Suite 260, Dept. DBP12345, San Mateo, CA 94403; 800/745-7323, fax 415/578-0672.

Integrated Circuits Conference in San Diego

If you had to miss ISSCC to attend Compton, don't despair, head for San Diego for the 15th annual *Custom Integrated Circuit Conference (CICC)* on May 9–12. The conference features a series of discussions on designing ICs for multimedia applications. Other papers discuss the latest research in areas such as microprocessors, programmable logic, video compression, digital synthesis, optoelectronics, and hard-disk and graphics controllers. Special "education sessions," scheduled for Saturday evening and Sunday morning, look at HDTV, filters, testability, quality issues, and other subjects. The conference proper lasts from Sunday evening until Wednesday and also includes a small exhibition.

Advance registration (by 4/23) costs \$215 for the conference or \$370 including the education sessions. Substantial discounts are available for IEEE members and students. Contact CICC '93, PO Box 3918, Frederick, MD 21705; 301/662-9400 ext. 216.

Combined Computing Research Event

While you're in San Diego, why not stay another week for the first ever *Federated Computing Research Conference* on May 14–22. This event combines several independent conferences, including the International Symposium on Computer Architecture, the ACM Symposium on Theory of Computing, the SIGPLAN Symposium on Parallel Programming, and various workshops on computational geometry, parallel algorithms, and similar topics.

Each of the individual events has its own registration fees of about \$200–\$400, with the normal discounts for IEEE or ACM members, students, and registrations before 4/18. Contact Event Registration Services, 407 Magee Avenue, Mill Valley, CA 94941; 800/388-1786 or 415/380-0405, fax 415/388-7506.

FPGA Applications Workshop in Napa Valley

The IEEE Computer Society will hold a workshop on *FPGAs for Custom Computing Machines (FCCM)* on April 5–7 in Napa, California. The workshop includes papers on applications of programmable logic in special-purpose processors or coprocessors. Subjects include accelerators, attached processors, FPGA-based computers, and software/programming issues.

On-site registration is \$325, or just \$245 for IEEE members. Contact the IEEE FCCM Workshop, c/o Duncan Buell, Supercomputing Research Center, 17100 Science Drive, Bowie, MD 20715; 301/805-7372.

Report on Analog-Digital Simulation

Dr. Hans Klein has written a report, *Mixed Analog-Digital Simulation*, that describes techniques for modeling and analyzing complex mixed-signal designs using standard CAE tools. The 212-page report costs \$299; contact Technology Information Publishing, 218 Coronado Drive, Aptos, CA 95003; 408/685-9217.