## At A Glance

**Pentium Approaches RISC Performance.....1** Intel's forthcoming 66-MHz Pentium chip doubles the performance of the 66-MHz 486DX2 on integer benchmarks and provides an even greater improvement for floating point. While this performance surpasses the weaker RISC chips, others hold a strong lead.

**Intel Reveals Pentium Implementation Details ......9** Pentium includes many improvements over the 486 design, including separate instruction and data caches, dual integer pipelines, a branch target buffer, a pipelined floating-point unit, and a 64-bit external bus. Software-visible changes, including new instructions, were kept to a minimum.

**Sun Details Extensive Plan for SPARC CPUs......20** Sun claims to be developing nine new processors, including extensions to its microSPARC and SuperSPARC chips as well as a new CPU family, UltraSPARC, which will debut in 1995.

**Oblique Perspective: Strategic Product Rescheduling.......28** John Wharton explains why the on-going delays in the Pentium program may be the best thing that could happen for Intel.

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## MICROPROCESSOR REPORT

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