Resources

COMPCON 93 in San Francisco

The IEEE's premiere computer convention is back, running from February 22–26 at the Stanford Court Hotel in San Francisco. The microprocessor session, chaired by our own John Wharton, will be on Tuesday and includes presentations on the 486SL, Hobbit, ARM, the 68060, and an extended session on PowerPC. New information on Pentium may also be disclosed (TBD). The parallel-processing session looks at Kendall Square's MPP machine, the Cray S-MP, and Sun's Dragon. Other sessions include multimedia, FPGAs, and high-speed workstations (featuring PA-RISC and Alpha). Both Monday and Friday will feature full-day tutorials, including Yale Patt on choosing a computer architecture.

Early registration (by 2/8) is $390 for the three-day conference and an additional $390 each for the tutorials. Substantial discounts are available for IEEE members and students. For more information, call 510/423-3490 or fax 510/423-9388. Electronic mail users can write to *compcon93@lbl.gov*.

Windows Hardware Conference in San Jose

The second annual Windows Hardware Engineering Conference will be held on March 1–3 at the San Jose Convention Center. The conference is aimed at engineers and managers who are developing hardware products designed to work with Microsoft Windows. The conference will provide the technical information needed to design state-of-the-art Windows PCs. General sessions will cover topics such as ease of use, local bus designs, optimizing display performance, SCSI implementations, and more. There will also be breakout sessions by various component vendors and a technical exhibition.

Registration for the three-day conference is $495 before 2/7, or $645 afterwards. For more information, call 800/421-6338 or 415/543-5847.

UC Berkeley Gives Fuzzy-Logic Short Course

The University of California at Berkeley is offering a series of short courses in computer engineering. The first, *Intelligent Fuzzy Systems: Design and Application,* is presented by Lotfi Zadeh, the "father of fuzzy logic," and others. It covers the fundamentals of fuzzy logic, knowledge-based systems, control systems, and adaptive fuzzy systems, as well as current hardware and software tools. The course will be held on February 24–26 in Redwood City. Other courses in the series cover topics such as object-oriented software and "standards as a competitive weapon."

Registration for the fuzzy logic class is $995. Other courses have different durations and fees. Contact UC Berkeley Extension, Dept. B, 2223 Fulton Street, Berkeley, CA 94720; 510/642-4111, fax 510/642-0374.

Multi-Chip Module Conference in California

The hot topic in chip packaging these days is multi-chip modules (MCMs) that combine several chips into a single package, which can improve clock speeds and reduce board space. The latest developments in this area will be covered at the IEEE's MCM Conference on March 15–18 in Santa Cruz, California. A set of tutorials is offered on Monday, while the remainder of the week features general sessions on MCM design, manufacturing, and testing, including a paper on a SuperSPARC MCM project. There will also be vendor exhibits and product demonstrations.

Advance registration (before 2/19) is $320 for the three-day conference, and $250 for the full-day tutorial or $135 for each half-day tutorial. Substantial discounts are available for IEEE members and students. Contact the IEEE Computer Society, Conference Department, 1730 Massachusetts Avenue NW, Washington, DC 20036; 202/371-1013, fax 202/728-0884.

