

At A Glance

Intel's 486SL Follows in 386SL's Footsteps	1
Intel unveiled the long-awaited 486SL, bringing 486-level performance to the portable market. The new chip includes power management, an integrated memory controller and an ISA bus interface.	
Pentium Fails Short of P5's Promises	3
Even though Pentium is late, it has already helped Intel fend off competition from RISC vendors. It won't fulfill Intel's promise of parity with RISCs, but this probably won't matter.	
Most Significant Bits	4
Intel 8-Mbit flash hit by one-year slip; AMD puts Intel on notice for 486 Microcode; Cyrix adds extended 486SLC; Cyrix reveals its first 486-pinout processor; Intel 960 wins key HP LaserJet design; Intel announces new interrupt controller; Sun extends product line at top and bottom; Die size errata	
Intel Seeking Royalties From System Makers	6
Intel is threatening PC makers who use non-Intel chips with patent infringement, based on an Intel memory management patent.	
MIPS Announces R4400 Offers 60-80% Performance Boost	7
The R4400 is an upgraded R4000 that doubles the on-board cache size and increases the maximum clock rate to 150 MHz (internal).	
Low-End PA7100LC Adds Dual Integer ALUs	9
HP's PA7100-LC, expected in 1993, combines a superscalar PA-RISC processor with an on-board memory controller to increase performance and reduce overall system cost.	
Motorola Details Plan to Extend 68K Line	14
Motorola gave an in-depth technical look at its plans for the 68060. The new chip, due in 1994, extends the 68040 with superscalar instruction dispatch and larger caches.	
Digital Plans Broad Alpha Processor Family	18
DEC outlined its plans for Alpha microprocessors through 1996. The company also announced the first Alpha systems, providing the first real performance data on the 21064 CPU.	
New MIPS Chip Targets Windows NT Boxes	21
QED's Orion chip will bring mainstream RISC performance to a new low price, using a die no bigger than a 486DX.	
IDT R3041 Lowers Embedded MIPS Entry Cost	23
Another low-cost MIPS chip, from Integrated Device Technology, aims for low-end laser printers and other embedded applications.	
EO Announces First Personal Communicator	25
EO's pen-based systems using the Hobbit CPU, represent the first products in a family of communications-oriented computers based on the AT&T chip set and PenPoint O/S.	
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