

# At A Glance

<b>MIPS Announces First ARC Systems</b> .....	<b>1</b>
MIPS has announced a line of R4000-based systems based on the ARC specification. While these systems should be competitive workstations, it won't be until the next generation that they have a chance of competing in the PC market.	
<b>A New World for Intel</b> .....	<b>3</b>
The 386/486 market is breaking wide open, and Intel's continued growth is dependent on fast market growth to offset its declining market share.	
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ARM announces ARM600 variant; SiS offers EISA chip set; Sun announces "Silicon Product Program"; Backers pull plug on Tera; Court rules disassembly violates copyright.	
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TI is developing a new DSP chip for IBM in support of the proposed Mwave multimedia standard. This new alliance has also provided DSP management and an API to the host OS as well as efficient multitasking of signal processing tasks.	
<b>Microcode Engine Offers Enhanced Emulation</b> .....	<b>12</b>
IMS is developing a microcode engine optimized for emulating the 386 and 68040 instruction sets. By implementing performance-critical functions in microcode, a major speed boost is promised.	
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## MICROPROCESSOR REPORT

THE INSIDERS' GUIDE TO MICROPROCESSOR HARDWARE

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*Microprocessor Report* is published every three weeks, 17 issues per year. Rates are: *N. America:* \$495 per year, \$895 for two years. *Europe:* £375 per year, £645 for two years. *Elsewhere:* \$595 per year, \$1,095 for two years. Additional copies in the same envelope: \$175 per year in North America, \$225 elsewhere. Canadian GST registration number: R140496720. Back issues are available.

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Published by  
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Winner, Computer Press Award, 1993, 1994



Printed on recycled paper with soy ink.