

At A Glance

Cyrix Joins x86 Fray with 386/486 Hybrid	1
Cyrix has made its long-rumored entry into the 386/486 microprocessor market with the 486SLC, a 386SX-pin-compatible processor with a 486-like integer core and a 1K cache.	
What Happened to MIPS?	3
The acquisition of MIPS by SGI is the end result of a long series of troubles at MIPS, some self-inflicted and some the result of attacks by powerful competitors and an overly vicious press.	
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386 prices fall; Intel announces its first 3.3-V processors; ARM announces ARM600 variant; Toshiba reveals RDRAM, Sync DRAM plans; AMD 29005 used in Apple laser printer; HP forms PA-RISC support organization.	
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Responding to all the other future-selling going on in the microprocessor business, MIPS has laid out its plans for the R4000 family through the middle of the decade.	
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Object-orientation is the software buzzword of the '90's, but what does it really accomplish?	
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Part 1 of a series on floating-point explains the mysteries of denormals, not-a-numbers, gradual underflow, and more.	
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